

深圳市金逸晨电子有限公司

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PRODUCT SPECIFICATION

Model No	TB154-07-08-B
Product Name	1.54 TFT LCD Module
Version	V1.0
Date	2023-06-16

- ☐ Preliminary Specification
☒ Final Specification

PREPARED BY	CHECKED BY	APPROVED BY

Revision History

Version	Date	Page	Revise record	Remarks
V1.0	2023.06.16	-	First issued	-

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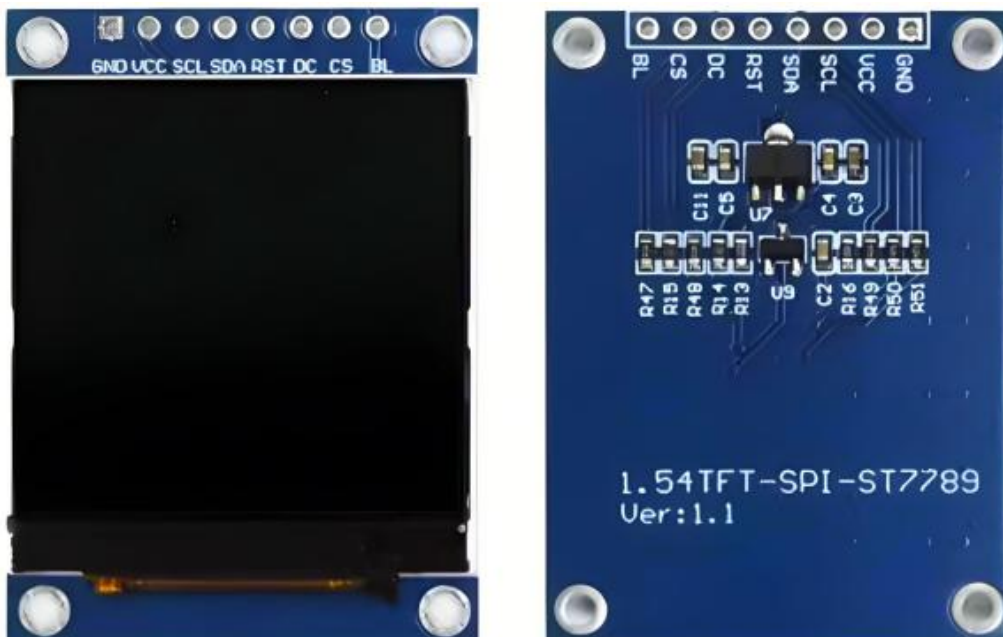
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1. General Parameter

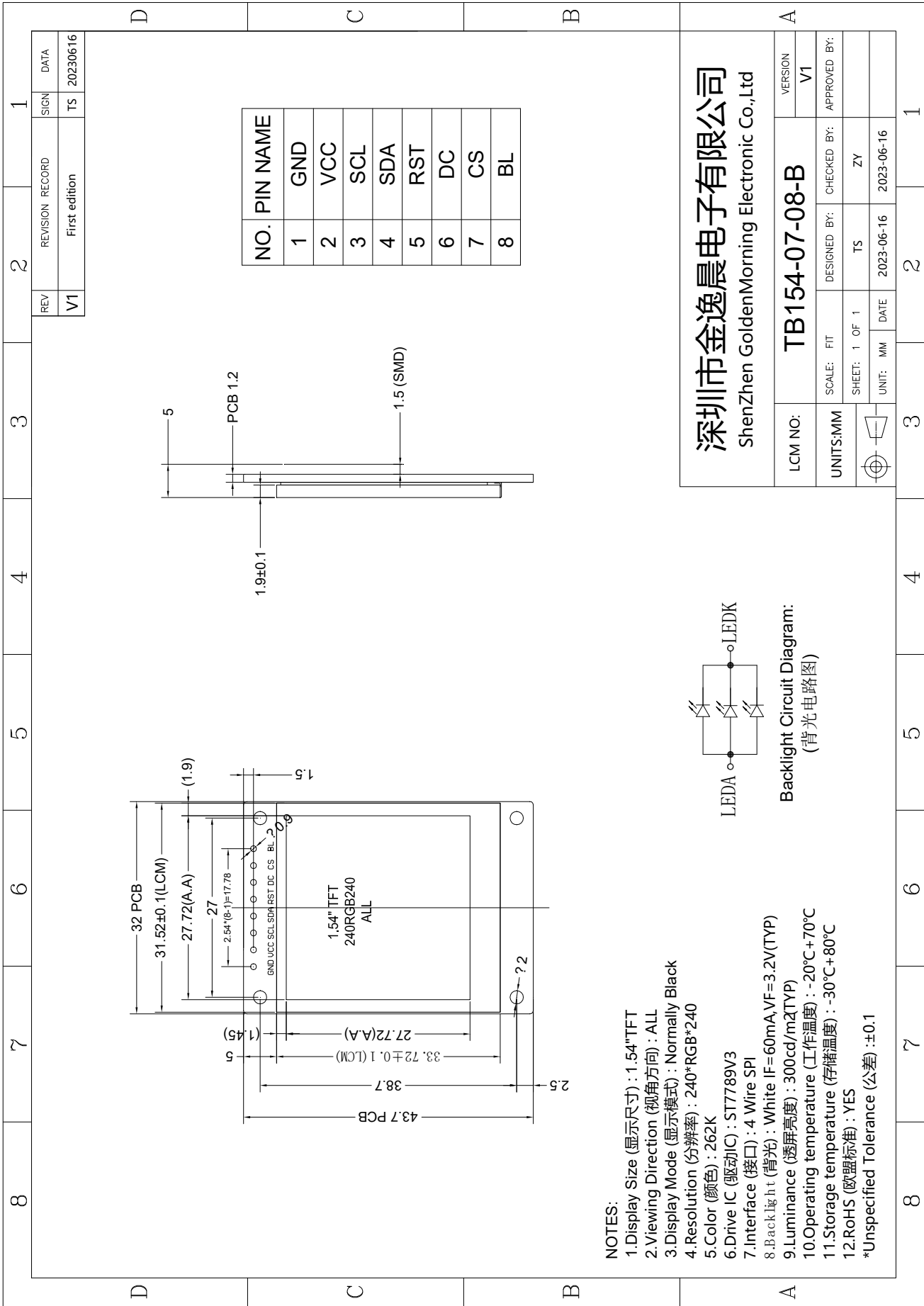
NO.	Features	Details	Unit
1	Display Size(Diagonal)	1.54"	Inch
2	LCD type	IPS TFT	-
3	Display Mode	Normally black	-
4	Resolution	240 RGB(H) x 240(V)	Pixels
5	Viewing Direction	ALL	-
6	Module size	32(W) x43.7(H) x 5(T)	mm
7	Active Area	27.72(W) x 27.72(H)	mm
8	Dot pitch	0.1155 (H) x 0.1155(V)	um
9	Color arrangement	RGB Vvertical stripe	-
10	Interface	4 Wire SPI	-
11	Display Colors	262K	-
12	Driver IC	ST7789	-
13	With or Without Touch Panel	Without	-
14	Operating Temperature	-20℃~70℃	℃
15	Storage Temperature	-30℃~80℃	℃
16	Weight	-	g

Note 1: Please refer to the mechanical drawing.

2. Physical drawing



3. Mechanical Dimension



4. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Analog Supply Voltage	VCC	-0.3	4.6	V
Logic Supply Voltage	IOVCC	-0.3	4.0	V
Operating temperature	Top	-20	70	°C
Storage temperature	Tst	-30	80	°C

Note 1: If tAmbient temperature below 50°C, the maximal humidity is 90%RH, if Ambient temperature over 50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

Note 3: If one of the above items is exceeded its maximum limitation momentarily, the quality of the product may be degraded. Absolute maximum limitation, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the recommend range.

5. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Analog Supply Voltage	VCC	2.4	2.75	3.3	V
Logic Supply Voltage	IOVCC	1.65	2.8	3.3	V
Input high voltage	V _{IN}	0.7*IOVCC	-	IOVCC	V
Input low voltage	V _{IL}	GND	-	0.3*IOVCC	V
Output high voltage	V _{OH}	0.8*IOVCC	-	IOVCC	V
Output low voltage	V _{OL}	GND	-	0.2*IOVCC	V
Input Current	I _{IN}	-	20	-	mA

6. Backlight Characteristic

Item	Symbol	Min.	Typ.	Max.	Unit	Condition
Forward Voltage	V _F	-	3.2	-	V	-
Forward Current	I _F	-	60	-	mA	-
Power dissipation	P _d	-	192	-	mW	-
LED Life Time(25 °C)	-	-	10000	-	Hrs	-

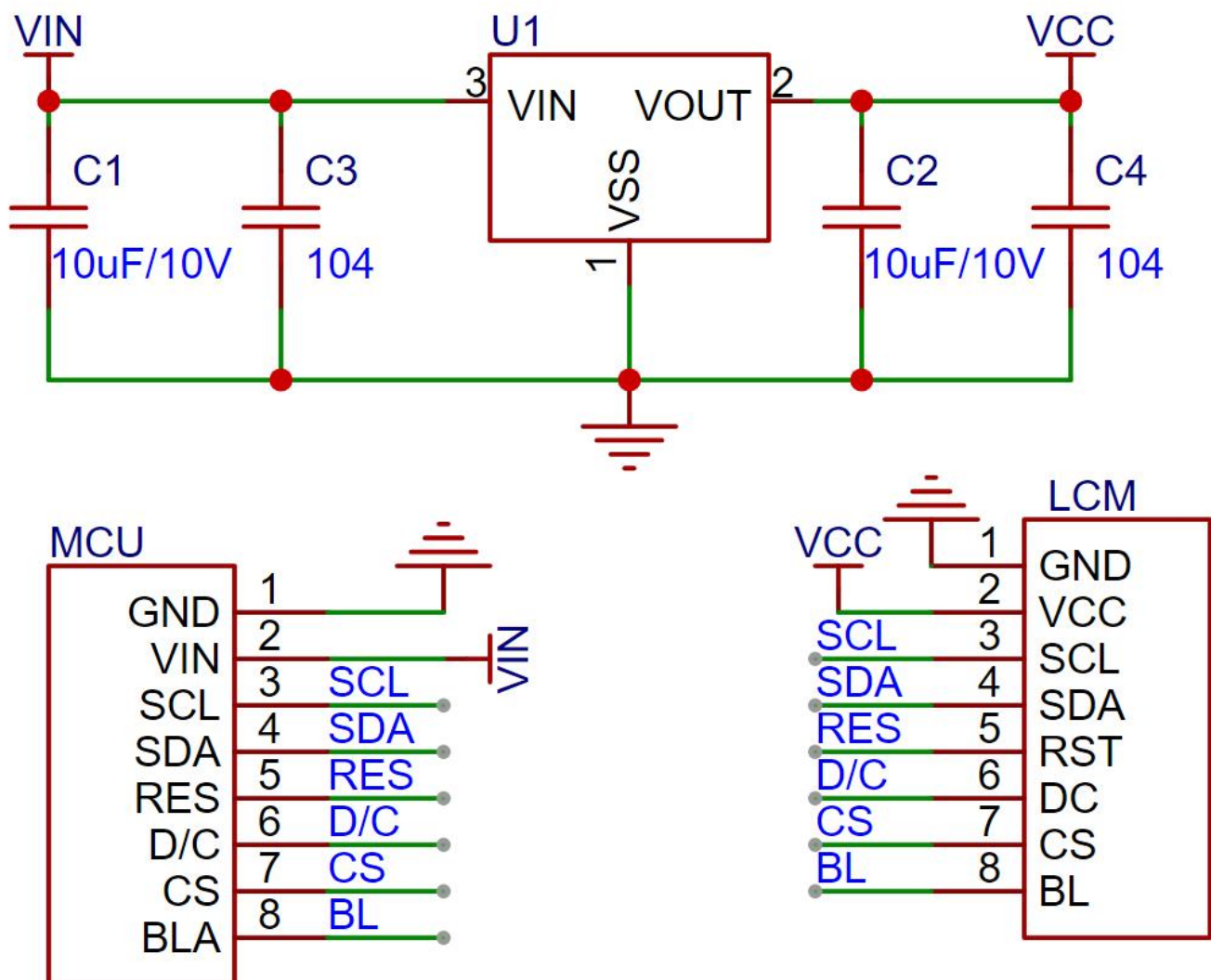
Note: LED life time defined as follows: The final brightness is at 50% of original brightness. Typical operating life time is estimated data.

7. Pins Definitions.

Pin.No	Symbol	Description
1	GND	Ground
2	VCC	Power supply for Analog
3	SCL	Serial clock pin.
4	SDA	SPI interface input/output pin
5	RST	LCM Reset pin
6	DC	Register select pin.
7	CS	Chip select pin ("Low" enable)
8	BL	Backlit

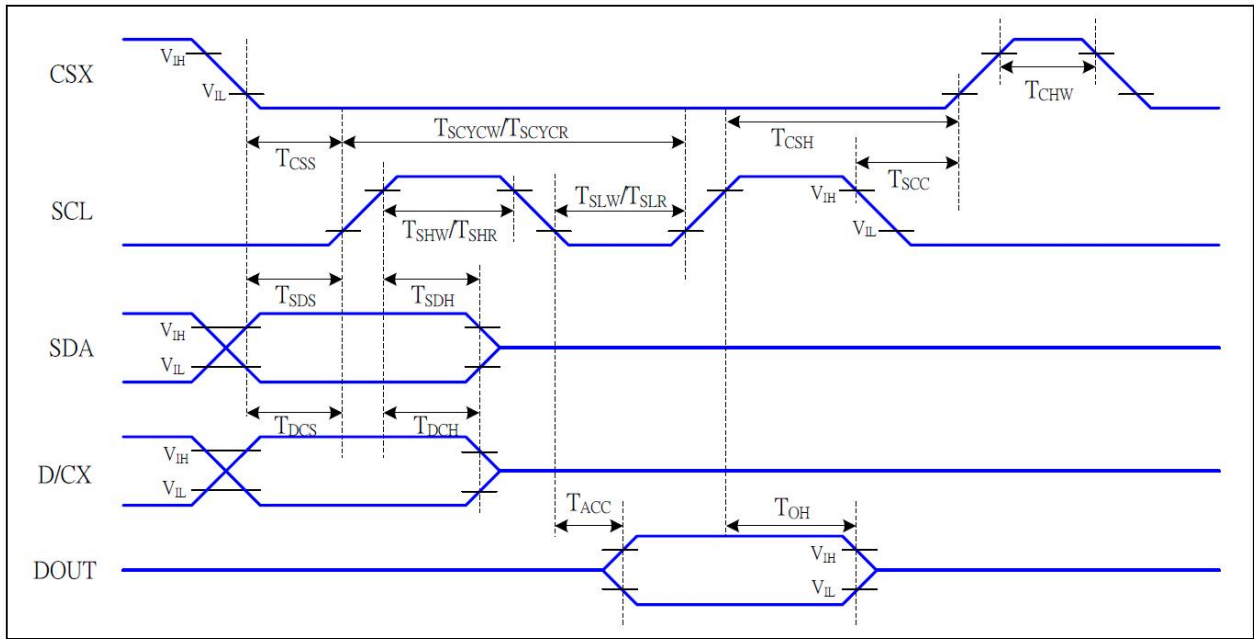
Note: The backlight LED is recommended to be powered independently by constant current drive.

8. Schematic Diagram



4SPI 参考原理图

9. Timing Characteristics



4-line serial Interface Timing Characteristics

VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=25°C

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	TBD	-	ns	
	T_{CSH}	Chip select hold time (write)	TBD	-	ns	
	T_{CSS}	Chip select setup time (read)	TBD	-	ns	
	T_{SCC}	Chip select hold time (read)	TBD	-	ns	
	T_{CHW}	Chip select "H" pulse width	TBD	-	ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	TBD	-	ns	-write command & data ram
	T_{SHW}	SCL "H" pulse width (Write)	TBD	-	ns	
	T_{SLW}	SCL "L" pulse width (Write)	TBD	-	ns	
	T_{SCYCR}	Serial clock cycle (Read)	TBD	-	ns	-read command & data ram
	T_{SHR}	SCL "H" pulse width (Read)	TBD	-	ns	
	T_{SLR}	SCL "L" pulse width (Read)	TBD	-	ns	
D/CX	T_{DCS}	D/CX setup time	TBD	-	ns	
	T_{DCH}	D/CX hold time	TBD	-	ns	
SDA (DIN)	T_{SDS}	Data setup time	TBD	-	ns	
	T_{SDH}	Data hold time	TBD	-	ns	
DOUT	T_{ACC}	Access time	TBD	TBD	ns	For maximum CL=30pF
	T_{OH}	Output disable time	TBD	TBD	ns	For minimum CL=8pF

4-line serial Interface Characteristics

Note1 : The rising time and falling time (T_r , T_f) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for Input signals.

10. Optical Characteristics

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Threshold Voltage		Vsat		4.1	4.3	4.5	V	Fig.1
		Vth		1.6	1.8	2.0	V	
Viewing Angle	Horizontal	Θ3	CR>10	75	80		°	Note 1
		Θ9		75	80		°	
	Vertical	Θ12		75	80		°	
		Θ6		75	80		°	
Contrast Ratio		CR	Θ= 0°	700	900			Note 2
Transmittance		T(%)	Θ= 0°	4.6	5.4			Note 3
NTSC		%	Θ= 0°	45	50			
Reproduction Of color	Red	Rx	Θ= 0°	0.601	0.631	0.661		Note 4 *CF glas s Wit h OC @C light
		Ry		0.300	0.330	0.660		
	Green	Gx		0.252	0.282	0.312		
		Gy		0.514	0.544	0.574		
	Blue	Bx		0.108	0.138	0.168		
		By		0.125	0.155	0.185		
Response Time		Tr+Tf	Θ= 0°		30	35	ms	Note 5

Note:

1.Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (See FIG.2).

2.Contrast measurements shall be made at viewing angle of Θ= 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIG. 2)

Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

4.The color chromaticity coordinates specified in Table1 shall be calculated from The spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the C/F. Measurement condition is C - light source

5.The electro-optical response time measurements shall be made as FIG.3 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Tf.

Figure 1. The definition of V_{th} & V_{sat}

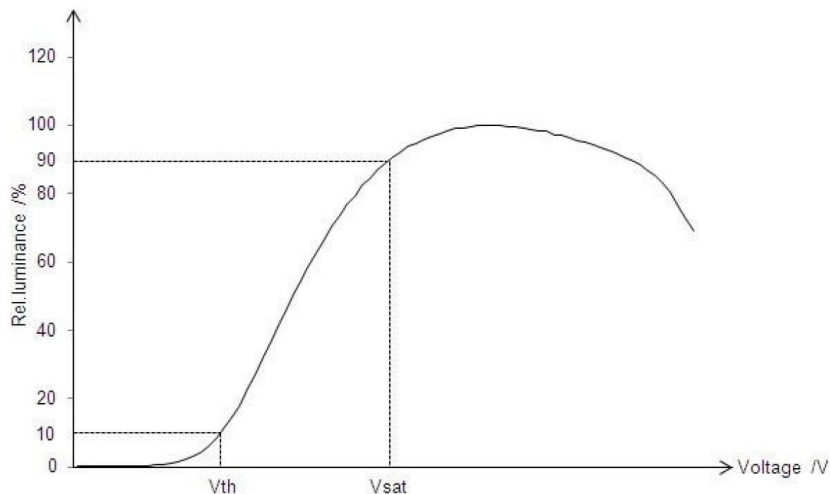


Figure 2. Measurement Set Up

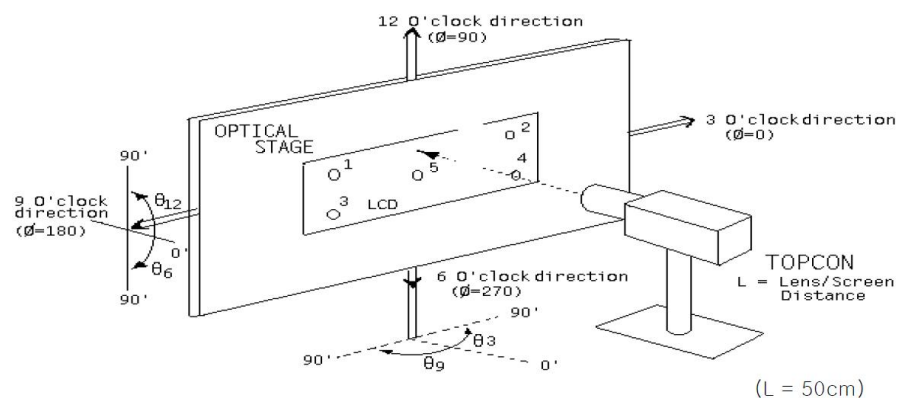
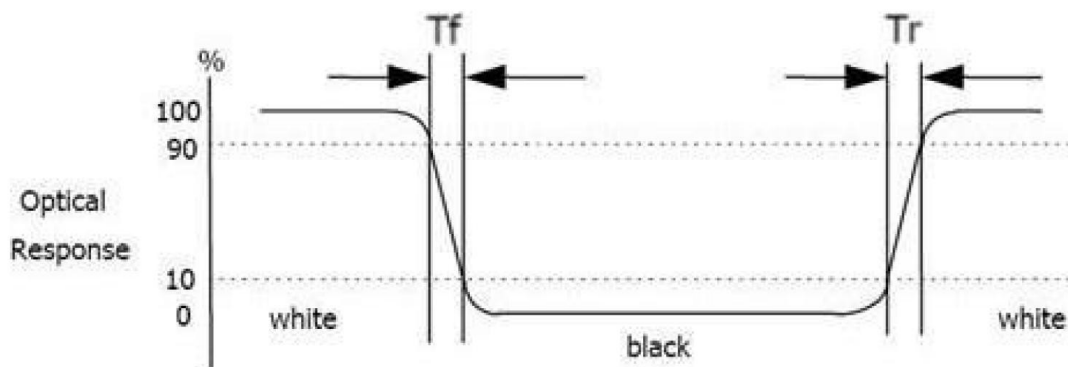


Figure 3. Response Time Testing



11. Reliability

11.1. MTBF

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal.

11.2. Test Condition

No	Item	Condition	Criteria
1	High Temperature Operating	70℃*24Hrs	1. No Defect Of Operational Function In Room Temperature Are Allowable. 2. IDD of LCM in Pre-and Post-Test Should Follow Specification.
2	Low Temperature Operating	-20℃*24Hrs	
3	High Humidity Storage	50℃*90%RH*24Hrs	
4	High Temperature Storage	80℃*24Hrs	
5	Low Temperature Storage	-30℃*24Hrs	
6	Thermal Cycling Test Storage	-	
7	Packing vibration	-	
8	Electrical Static Discharge	-	
		-	
9	Drop Test (Packaged)	-	

Note1. The test samples should be applied to only one test item.

Note2. Sample size for each test item is 2pcs.

Note3. No defection function allowable.

12. Precautions

12.1. Storage Conditions

- 12.1.1. Store the panel or module in a dark place where the temperature is $23\pm5^{\circ}\text{C}$ and the humidity is below $45\pm20\%\text{RH}$.
- 12.1.2. Store in anti-static electricity container.
- 12.1.3. Store in clean environment, free from dust, active gas, and solvent.
- 12.1.4. Do not place the module near organics solvents or corrosive gases.
- 12.1.5. Do not crush, shake, or jolt the module.
- 12.1.6. Strong light exposure causes degradation of polarizer and color filter.

12.2. Handling Precautions

- 12.2.1. Avoid static electricity, which can damage the CMOS LSI.
- 12.2.2. The polarizing plate of the display is very fragile, please handle it very carefully.
- 12.2.3. Do not give external shock.
- 12.2.4. Do not apply excessive force on the surface.
- 12.2.5. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.
- 12.2.6. Reverse and use within ratings in order to keep performance and prevent damage
- 12.2.7. Do not remove the panel or frame from the module.
- 12.2.8. Except for soldering the interface, do not make any alterations or modifications with a soldering iron; Ensure welding temperature at 320°C to 350°C , the welding time control within the 10 s, welding note don't stay too long in the same place to avoid scald FPC.

12.3. Limited Warranty

- 12.3.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss. (我们的保证责任仅限于修理和/或更换。我们将不负任何相应的损失。)
- 12.3.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used. (如果可能, 我们建议客户在 6 个月内用完所有模块。如果模块存放时间超过 12 个月, 我们建议在使用模块前重新检查。)
- 12.3.3. The warranty period is twelve months from the date of delivery. Buyer shall complete the assembly of all processes within twelve months of validity. During the warranty period, if the product quality problems, our company will be responsible for repair and replacement. All products must be stored and handled in accordance with regulations. Under warranty. When the goods do not comply with the above terms, we do not provide warranty services. (保修期为交货之日起十二个月。买方应在 12 个月内完成所有流程的组装。在保修期内, 如果产品出现质量问题, 我公司将负责维修和更换。所有产品必须按照规定储存和处理。在保修期内。当货物不符合上述条款时, 我们不提供保修服务。)